

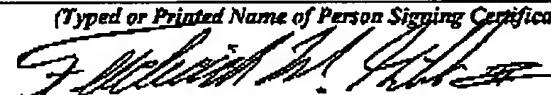
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CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)			
Applicant(s): Allen et al.			
Application No. 10/604,987	Filing Date 08/29/03	Examiner Siek, Vuthe	Docket No. BUR920030092US1
Invention: THE USE OF A LAYOUT-OPTIMIZATION TOOL TO INCREASE THE YIELD AND RELIABILITY OF VLSI DESIGNS			
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Application Number	10/604,987
Filing Date	08/29/03
First Named Inventor	Allen et al.
Art Unit	2822
Examiner Name	Siek, Vuthe
Attorney Docket Number	BUR920030092US1

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4/6/05

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Frederick W. Gibb, III

301-261-8071

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